In the Specification:

[0036]

Please AMEND paragraph [0039] as follows:

[0036]

[0039] Devices 110 and 120 each have a communications range that is defined by a coverage area. In the environment of FIG. 1 devices 110 and 120 are within each other's communications ranges for performing both UWB and Bluetooth communications. Accordingly, a first wireless communications link 140 and a second wireless communications link 130 are established between devices 110 and 120. As discussed, these links may be of different types. For instance, first link 140 may be a Bluetooth link or channel (hereinafter Bluetooth link or BT link), while second link ~~140~~ 130 may be a UWB link or channel (hereinafter UWB link).

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[0037]

Please AMEND paragraph [0040] as follows:

[0037]

[0040] According to an aspect of the present invention, first device 110 and second device 120 may communicate data, such as payload data, across second link(s) ~~140~~ 130 and may communicate control data such as error control data for error control processing of the UWB communications across first link(s) ~~130~~ 140. Such error control data may include, for example, retransmission control data employed in Automatic Repeat Request (ARQ) scheme or mechanism. Other types of data may also be communicated across first link(s) ~~130~~ 140 as desired. Data communications across second link ~~140~~ 130 may be unilateral or bilateral.

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T. T.

[0048]

Please AMEND paragraph [0051] as follows:

[0048]

~~[0051]~~ When the device of FIG. 2 engages in UWB communications, it employs the services of UWB segment 230. As shown in FIG. 2, UWB segment 230 includes a UWB module ~~218~~ 234, a UWB transceiver ~~220~~ 238, and an antenna ~~222~~ 240.

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[0049]
Please AMEND paragraph [0052] as follows:
[0049]
[0052] UWB module 218 234 provides for the exchange of information across UWB links according to one or more protocol layers. For example, UWB module may provide session management functionality to manage various UWB sessions. In addition, UWB module 218 234 may perform baseband processing, such as error correction encoding and decoding. In addition, UWB module 218 234 perform various link level protocols with remote devices according to physical layer protocols. Examples of such protocols include retransmission protocols such as the automatic repeat request (ARQ) protocol.

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[0050]
Please AMEND paragraph [0053] as follows:
[0050]
[0053] UWB transceiver 220 238 is coupled to antenna 222 240. UWB transceiver 220 238 includes electronics, which allow the device of FIG. 2 (in conjunction with antenna 222 240) to exchange wireless UWB signals with devices, such as remote device 120. For the transmission of UWB signals, such electronics may include a pulse generator. For the reception of UWB signals, such electronics may include timing circuitry and filters.

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[0051]
Please AMEND paragraph [0054] as follows:
[0051]
[0054] The architecture of FIG. 2 may be implemented in hardware, software, firmware, or any combination thereof. One example of such implementation is shown in FIG. 3. This implementation includes a processor 310, a memory 312, and a user interface 314. In addition, the implementation of FIG. 3 includes Bluetooth transceiver 218, antenna 220, UWB

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transceiver ~~220~~ 238, and antenna ~~222~~ 240. Transceivers ~~214~~ 218 and ~~220~~ 238 may be

implemented as described above with reference to FIG. 2.

[0052]

Please AMEND paragraph [0055] as follows:

[0052]

[0055] As shown in FIG. 3, processor 310 is coupled to transceivers ~~214~~ 218 and

~~220~~ 238. Processor 310 controls device operation. Processor 310 may be implemented with one

or more microprocessors that are each capable of executing software instructions stored in

memory 312.

[0053]

Please AMEND paragraph [0056] as follows:

[0053]

[0056] Memory 312 includes random access memory (RAM), read only memory

(ROM), and/or flash memory, and stores information in the form of data and software

components (also referred to herein as modules). These software components include

instructions that can be executed by processor 310. Various types of software components may

be stored in memory 312. For instance, memory 312 may store software components that

control the operations of transceivers ~~214~~ 218 and ~~220~~ 238. Also, memory 312 may store

software components that provide for the functionality of host 202, HCI interface 212, link

manager 214, link controller 216, interface 232 and UWB module 234.

[0055]

Please AMEND paragraph [0058] as follows:

[0055]

[0058] The elements shown in FIG. 3 may be coupled according to various

techniques. One such technique involves coupling transceivers ~~214~~ 218 and ~~220~~ 238, processor

310, memory 312, and user interface 314 through one or more bus interfaces. In addition, each of

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these components is coupled to a power source, such as a removable and rechargeable battery pack (not shown).

[00063]

Please AMEND paragraph [00067] as follows:

[00063]

[00067] Data packets are then sent to transmission buffer 404 to await transmission. As shown in FIG. 4, transmission buffer 612 includes an input port (labeled "IN"), an output port (labeled "OUT"), and a gate port (labeled "G"). Transmission buffer 404 receives data packets and stores them in memory. Transmission buffer 404 outputs a stored packet when it receives signal at its gate port. Transmission buffer 612 404 may store and output packet in a first-in, first-out (FIFO) manner or selectively, as desired. .

[00067]

Please AMEND paragraph [00071] as follows:

[00067]

[00071] As described above, transmissions and retransmissions may be initiated by signals from retransmission controller 408 or the like to transmission buffer 404 or retransmission buffer 406. Retransmission controller 608 408 receives error control data for UWB communications from the BT link and controls such transmission and retransmission accordingly. Various retransmission schemes or mechanisms may be employed in conjunction with the method and system herein to address transmission errors across the UWB link. One such approach is ARQ which employs feedback control to request, actively or passively, retransmission of corrupted data. Accordingly, the error control data may be an Acknowledgment (ACK) or Non-Acknowledgment (NAK) depending on the ARQ scheme or mechanism. An example of an ACK packet is discussed further below with reference to FIG. 7.